

25G SFP28 LR Rx 10km Industrial Single-Receiver Optical Module GSSR-SPO250-LRT

Features

- Hot-pluggable SFP28 form factor
- PIN photo-detector receiver without transmitter
- Internal CDR on receiver channel
- Compliant with SFP28 MSA and IEEE 802.3cc 25GBASE-LR
- Compliant CPRI/eCPRI specifications
- Data rate up to 25.78125Gbps
- Reach up to 10km over SMF
- Power consumption < 1W
- LC receptacle
- Operating case temperature range -40°C to +85°C
- 3.3V power supply voltage
- RoHS compliant (lead free)



- 25GBASE-LR Ethernet
- CPRI Option 10
- eCPRI

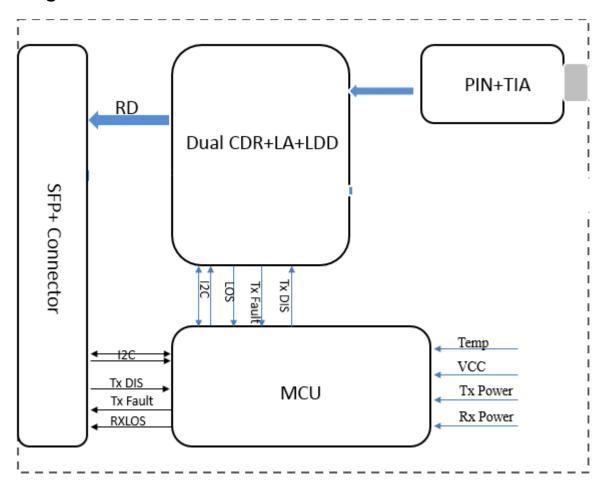
Description

The Gigalight 25G SFP28 LR Rx 10km industrial single-receiver optical module (GSSR-SPO250-LRT) is designed for 25GBASE-LR Ethernet and CPRI/eCPRI Deep Packet Inspection (DPI) links reach up to 10km over Single-Mode Fiber (SMF). The high-performance module operates at 25.78125Gbps using a nominal wavelength of 1310nm. The electrical interface uses a 20-contact edge type connector. The optical interface uses LC receptacle. The module incorporates Gigalight's proven circuit and technology to provide reliable long life, high performance, and consistent service.





Block Diagram



Absolute Maximum Ratings

| Parameter | Symbol | Min | Max | Unit |
|---------------------|--------|-----|-----|------|
| Supply Voltage | Vcc | 0 | 3.6 | V |
| Storage Temperature | Ts | -40 | +85 | °C |
| Relative Humidity | - | 5 | 85 | % |

Recommended Operating Conditions

| Parameter | Symbol | Min | Typical | Max | Unit |
|----------------------------|--------|-------|---------|-------|------|
| Operating Case Temperature | Тс | -40 | | +85 | °C |
| Power Supply Voltage | Vcc | +3.13 | +3.3 | +3.47 | V |
| Power Supply Current | Icc | | | 303 | mA |

Electrical Specifications

| Parameter | Symbol | Min | Typical | Max | Unit |
|--|------------------|----------------------|---------|----------|-------|
| Differential Output Impedance | Z _{out} | 90 | 100 | 110 | ohm |
| Differential Output Voltage Amplitude ¹ | ΔV_{out} | 500 | | 800 | mVp-p |
| Input Logic Level High | V _{IH} | 2.0 | | V_{cc} | V |
| Input Logic Level Low | V _{IL} | 0 | | 0.8 | V |
| Output Logic Level High | V _{OH} | V _{cc} -0.5 | | V_{cc} | V |
| Output Logic Level Low | V _{OL} | 0 | | 0.4 | V |

Notes:

Optical Characteristics

| Parameter | Symbol | Min | Typical | Max | Unit | Notes | |
|-------------------------------------|-------------------|------|---------|------|------|-------|--|
| Receiver | | | | | | | |
| Data rate | BR | | 25.78 | | Gbps | | |
| Centre Wavelength | λ_{c} | 1295 | 1310 | 1325 | nm | | |
| Average Power at Receiver | | | | 2 | dBm | | |
| Receive reference (max) | | | | -26 | dB | | |
| Receiver Sensitivity (OMA) | P _{sens} | - | - | -12 | dBm | 1 | |
| Stressed receiver sensitivity (OMA) | | | | -9.5 | dBm | 2 | |
| LOS Assert | LOSA | -30 | | | dBm | | |
| LOS De-Assert | LOS _D | | | -13 | dBm | | |
| LOS Hysteresis | | 0.5 | | | dB | | |

Notes:

- 1. For 25G-LR with FEC, receiver sensitivity is defined at BER=5E-5, not 1E-12.
- 2. Measured with conformance test signal at TP3 for BER=5E-5.

^{1.} Differential output voltage amplitude is measured between RxnP and RxnN.



Timing and Electrical

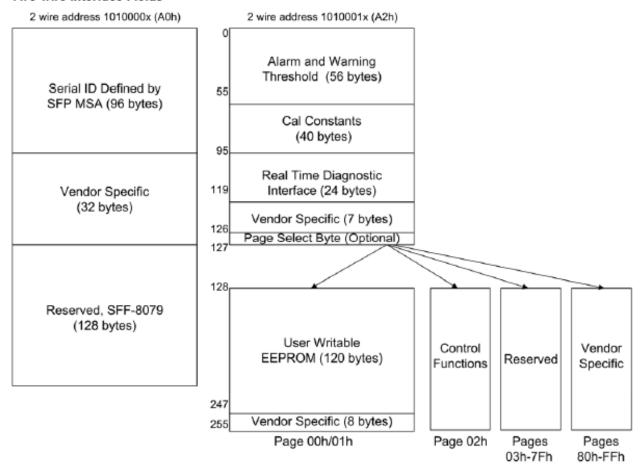
| Parameter | Symbol | Min. | Max. | Unit | Conditions |
|---|-------------------|------|------|------|--|
| Time to initialize 2-wire interface | t_2w_start_up | | 300 | ms | From power on or hot plug after the supply meeting <u>Table 8</u> . |
| Time to initialize | t_start_up | | 300 | ms | From power supplies meeting <u>Table 8</u> or hot plug or Tx disable negated during power up, or Tx_Fault recovery, until non-cooled power level I part (or non-cooled power level II part already enabled at power level II for Tx_Fault recovery) is fully operational. |
| Time to initialize cooled module and time to power up a cooled module to Power Level II | t_start_up_cooled | | 90 | 5 | From power supplies meeting Table 8 or hot plug, or Tx disable negated during power up or Tx_Fault recovery, until cooled power level I part (or cooled power level II part during fault recovery) is fully operational. Also, from stop bit low-to-high SDA transition enabling Power Level II until cooled module is fully operational |
| Time to Power Up to Level II | t_power_level2 | | 300 | ms | From stop bit low-to-high SDA transition enabling power level II until non-cooled module is fully operational |
| Time to Power Down from Level II | t_power_down | | 300 | ms | From stop bit low-to-high SDA transition dis- abling power level II until module is within power level I requirements |
| Rx_LOS assert delay | t_los_on | | 100 | μs | From occurrence of loss of signal to assertion of Rx_LOS |
| Rx_LOS negate delay | t_los_off | | 100 | μs | From occurrence of presence of signal to negation of Rx_LOS |

Memory Organization

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The memory map specific data field defines as following.

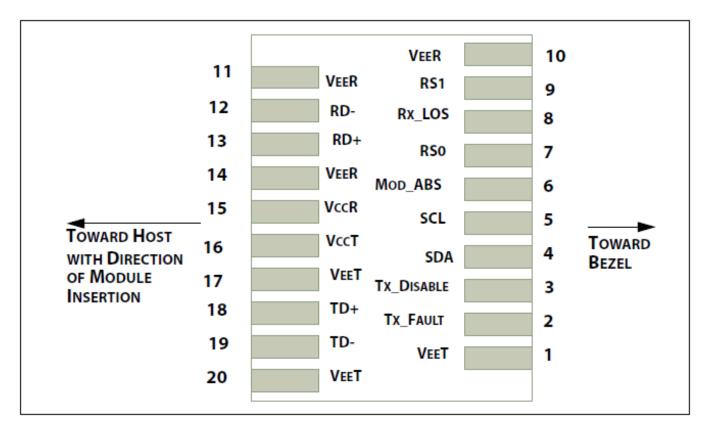
Two-wire Interface Fields

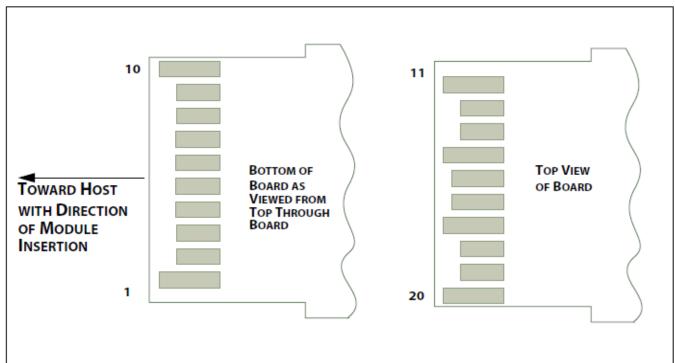


TWO-WIRE INTERFACE FIELDS



Pin Definitions







Pin Descriptions

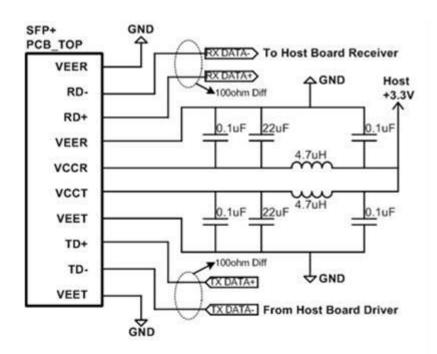
| PIN | Logic | Symbol | Name / Description | Note |
|-----|-----------|----------|---|------|
| 1 | | VeeT | Module Transmitter Ground | 1 |
| 2 | LVTTL-O | TX_Fault | Module Transmitter Fault | 2 |
| 3 | LVTTL-I | TX_Dis | Transmitter Disable; Turns off transmitter laser output | |
| 4 | LVTTL-I/O | SDA | 2-Wire Serial Interface Data Line | 2 |
| 5 | LVTTL-I | SCL | 2-Wire Serial Interface Clock | 2 |
| 6 | | MOD_ABS | Module Definition, Grounded in the module | |
| 7 | LVTTL-I | RS0 | Receiver Rate Select | |
| 8 | LVTTL-O | RX_LOS | Receiver Loss of Signal Indication Active LOW | |
| 9 | LVTTL-I | RS1 | Transmitter Rate Select (not used) | |
| 10 | | VeeR | Module Receiver Ground | 1 |
| 11 | | VeeR | Module Receiver Ground | 1 |
| 12 | CML-O | RD- | Receiver Inverted Data Output | |
| 13 | CML-O | RD+ | Receiver Data Output | |
| 14 | | VeeR | Module Receiver Ground | 1 |
| 15 | | VccR | Module Receiver 3.3 V Supply | |
| 16 | | VccT | Module Receiver 3.3 V Supply | |
| 17 | | VeeT | Module Transmitter Ground | 1 |
| 18 | CML-I | TD+ | Transmitter Non-Inverted Data Input | |
| 19 | CML-I | TD- | Transmitter Inverted Data Input | |
| 20 | | VeeT | Module Transmitter Ground | 1 |

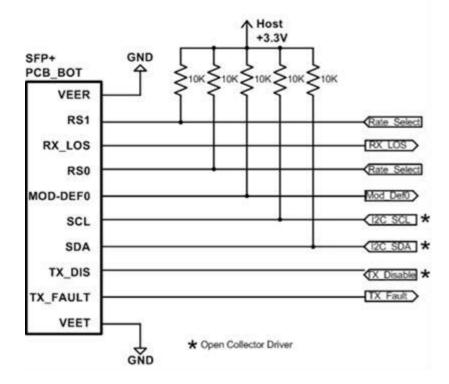
Notes:

- 1. Module ground pins GND are isolated from the module case.
- 2. Shall be pulled up with 4.7K-10K ohms to a voltage between 3.15V and 3.45V on the host board.



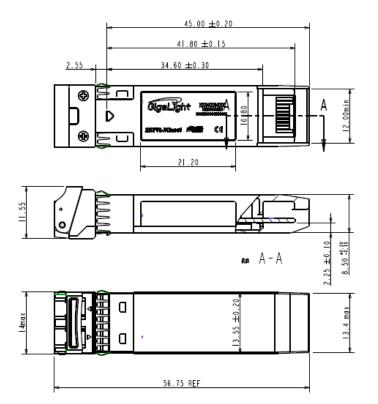
Recommended Interface Circuit

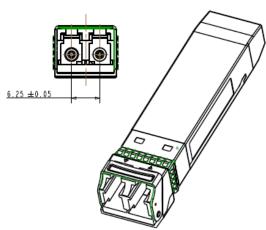






Mechanical Dimensions





Regulatory Compliance

The Gigalight GSSR-SPO250-LRT transceivers are Class 1 Laser Products. They are certified per the following standards:

| Feature | Standard |
|--------------------------|--|
| Laser Safety | IEC 60825-1:2014 (Third Edition) |
| Environmental protection | 2011/65/EU |
| CE EMC | EN55032:2015 EN55035:2017 EN61000-3-2:2014 EN61000-3-3:2013 |
| FCC | FCC Part 15, Subpart B; ANSI C63.4-2014 |

References

- 1. SFP28 MSA
- 2. Ethernet IEEE 802.3cc
- 3. Directive 2011/65/EU of the European Parliament and of the Council, "on the restriction of the use of certain hazardous substances in electrical and electronic equipment," July 1, 2011.



Use of controls or adjustment or performance of procedures other than those specified herein may result in hazardous radiation exposure.

Ordering information

| Part Number | Product Description |
|-----------------|--|
| GSSR-SPO250-LRT | 25G SFP28 LR Rx 10km, Single-Receiver Only, -40°C to +85°C |

Important Notice

Performance figures, data and any illustrative material provided in this data sheet are typical and must be specifically confirmed in writing by Gigalight before they become applicable to any particular order or contract. In accordance with the Gigalight policy of continuous improvement specifications may change without notice.

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Optical Interconnection Design Innovator

Revision History

| Revision | Date | Description |
|----------|--------------|------------------|
| VO | July-20-2019 | Advance Release. |